### **SRS PWM**

## System Requirements Specification



Status: Draft

Author: automatically generated from the SiSy model

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#### 1 Purpose

All elements of this project are parts of a course for the professional development of embedded systems. This Embedded Systems Engineering course is intended to develop a broad interdisciplinary understanding and knowledge of the participants as well as to develop practical skills for the realization of embedded systems.

The hardware platform for this course is the mySTM32 Board lite. It has a microcontroller of the STM32 family and all required input and output devices or add-ons.

#### 2 Overall description of the task

A microcontroller application is to be developed in which an LED fades in and out gently for the human eye. The PWM frequency must be flicker-free and the fading in and out of the LED should be clearly perceptible. Fading in and out can also be done non-linearly.

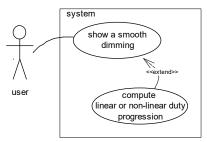


figure 1: uc: PWM tasks, user's perspective

List of top level requirements:

- system: show a smooth dimming
- system: compute linear or non-linear duty progression

### 3 Functional requirements

When the microcontroller is started, the PWM frequency is configured to 1000 Hz. As long as the system is switched on, the new duty value is calculated (linear or not linear) and the LED is dimmed with this value. The cycle duration is set with a short waiting routine.

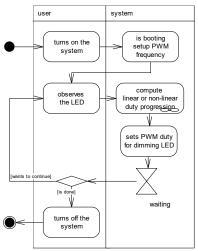


figure 2: show a smooth dimming



#### 4 Hardware requirements

The hardware platform for this course is the mySTM32 Board lite. It has a microcontroller of the STM32 family and all required input and output devices or add-ons.

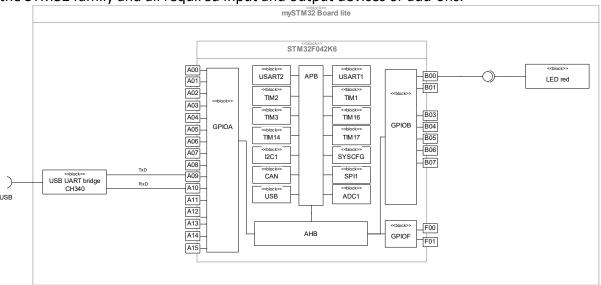


figure 3: PWM HRM

connected pinB0 : LED red

connected USB

### 5 Process requirements

A software process is the defined sequence of activities, the agreed rules, techniques, tools and the expected results of the activities for the production of software. Defined software processes ensure the plannability, controllability and quality of results in the manufacture of software. The following simple software process is agreed as a binding workflow for this course.

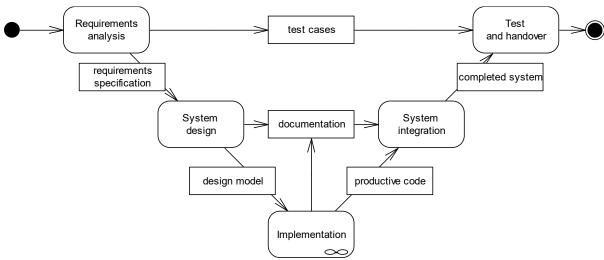


figure 4: act: lightweight model driven embedded software process







#### table 1: lightweight model driven embedded software process

Activity	Expected results
Requirements analysis	<ul> <li>User's perspective as use case diagram (as SysML / UML model)</li> <li>required functionalities as activity diagrams (as SysML / UML model)</li> <li>Test cases (as a document)</li> <li>HRM hardware resource model (as SysML model)</li> <li>SRS System Requirements Specification (as a document)</li> </ul>
System design	<ul> <li>Class model of the concept level / architecture model (as UML model)</li> <li>if necessary, state model (as UML model)</li> <li>System documentation (as a document)</li> </ul>
Implementation	<ul> <li>Class model of the realization (as UML model)</li> <li>Behavioral models of the realization (as UML model)</li> <li>Productive code (as a transferable format of the target platform, * .hex, * .elf)</li> <li>System documentation (as a document)</li> </ul>
System integration	<ul><li>hardware software integration</li><li>the completed system</li></ul>
Test and handover	<ul><li>the tested system</li><li>the technical system documentation (as a document)</li><li>the user documentation (as a document)</li></ul>

## 6 Attachment

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